



Application No. 09/936,032  
Filed 03/11/2002

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*Erwin J. Basinski*  
(Erwin J. Basinski)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Inventor Michael NICOLAIDIS

Serial No.: 09/936,032

Filing Date: March 11, 2002

For: LOGIC CIRCUIT PROTECTED  
AGAINST TRANSITORY  
PERTURBATIONS

Examiner: GANDHI, Dipakkumar B

Group Art Unit: 2133

**AMENDMENT UNDER 37 C.F.R. §1.121**

Mail Stop Amendment  
Commissioner for Patents  
P. O. Box 1450  
Washington, D.C. 22313-1450

Dear Sir:

This is in response to the Office Action dated July 14, 2004 for which a response was due by October 14, 2004. Accordingly, a three month extension of time is hereby requested under 37 CFR § 1.136(a), extending the time for response to January 14, 2005, and therefore this response is timely filed. The required fee for the 3 month extension of time to respond is enclosed.

Please enter the following amendments, which Applicant believes places the application and claims in condition for allowance:

Page 2 Amendments to the Specification

Page 3 Amendments to the Claims

Page 5 Remarks Section

Attachment Copy of the Examiner's Interview Summary dated 29 July 2004